

# Data Sheet

Customer:

Product: Multilayer Chip Ceramic Capacitors – Untra Small Size

Size : 0201

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### Record of change

Date	Ver.	Description	Page

## HITANO ENTERPRISE CORP.

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### Application

- ♦ Miniature microwave module.
- ♦ Portable equipments (ex. Mobile phone, PDA).
- ♦ High frequency circuits.

### **General Specification**

Dielectric	NPO	X7R	X5R			
Size	0201					
Capacitance*	0.3pF to 100pF	100pF to 10nF	100pF to 0.22µF			
Capacitance tolerance	J(±5%), K(±10%)	K(±10%), M(±20%)	K(±10%), M(±20%)			
Rated voltage (WVDC)	16V, 25V, 50V	6.3V, 10V, 16V, 25V, 50V	6.3V,10V, 16V,25V,50V			
Q*/D.F.	Cap<30pF, Q ≥ 400+20C Cap ≥ 30pF, Q ≥ 1000 Ur=16V,25V: ≤ 3.5% Ur=10V: ≤ 5.0% Ur=6.3V:		Ur=50V:≤3.0% Ur=16V,25V: ≤ 3.5% Ur=10V:≤5.0% Ur=6.3V:≤ 10%			
Insulation resistance at Ur	≥ 10G Ω	≥ 10G $\Omega$ or RxC ≥ 500 $\Omega$ xF whi	chever is less			
Operating temperature	-55 to +125°C	°C -55 to +85°C				
Capacitance change	±30ppm	±15%				
Termination	Cu(or Ag)/Ni/Sn (lead-free termination)					

 $^{\ast\ast}$  Measured at the conditions of 30~70% related humidity.

NP0: Apply 1.0  $\pm$  0.2Vrms, 1.0MHz  $\pm$  10% at the condition of 25°C ambient temperature

X7R/X5R: Apply 1.0  $\pm$  0.2Vrms, 1.0kHz  $\pm$  10% at the condition of 25°C ambient temperature

Preconditioning for Class II MLCC: Perform a heat treatment at 150  $\pm$  10°C for 1 hour, then leave in ambient condition

for 24 ± 2 hours before measurement.

#### How to order:

<u>C</u>	<u>0201</u>	<u>N</u>	<u>100</u>	J	<u>500</u>	<u>N</u>	<u>v</u>
<u>Series</u>	<u>Size</u>	<b>Dielectric</b>	Capacitance	Tolerance	Rated voltage	Termination	Packaging
C= series	0201	N=NP0 (C0G) B=X7R X=X5R	Two significant digits followed by no. of zeros. And R is in place of decimal point. eg.: 0R5=0.5pF 1R0=1.0pF 100=10x10 <sup>0</sup>	J=±5% K=±10% M=±20% Z=+80-20%	Two significant digits followed by no. of zeros. And R is in place of decimal point. <b>160=</b> 16 VDC <b>250=</b> 25 VDC <b>500=</b> 50 VDC	N=Nickel barrier with 100 % Tin	V=7" reeled
			100=10x10 <sup>0</sup> =10pF		<b>500</b> =50 VDC		

### **Dimension:**



Si: Inc	ze ch(mm)	L(mm)	W(mm)	T(mm) Symbol (T)	M₀(mm)	
02	01	0.60±0.03	0.30±0.03	0.30±0.03	0.15±0.05	

### Packaging Q'TY:

Size	Thickness (mm) Symbol (T)	7 inch Paper Tape
0201	0.30±0.03	15,000pcs

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## Capacitance Range:

	SIZE						0201							
	DIELECTRIC			X7R					X5R			COG		
RATED VOLTAGE		6.3V	10V	16V	25V	50V	6.3V	10V	16V	25V	50V	16V	25V	50V
	100pF (101)			Т	Т	Т					Т			
	150pF (151)			Т	Т	Т								
	180pF (181)			Т	Т	Т								
	220pF (221)			Т	Т	Т					Т			
	330pF (331)			Т	Т	Т								
	470pF (471)			Т	Т	Т					Т			
	560pF (561)			Т	Т	Т								
	680pF (681)			Т	Т	Т								
	1000pF (102)	Т	Т	Т	Т	Т					Т			
	1500pF (152)	Т	Т	Т	Т									
	2200pF (222)	Т	Т	Т	Т									
	3300pF (332)	Т	Т	Т	Т									
	4700pF (472)	T	Т	T	Т									
	5600pF (562)	T	T	T	T									
	6800pF (682)	T 	T 	T -										
	8200pF (822)	T	T	Т										
	10nF (103)	Т	Т	Т	Т		Т		Т	Т	Т			
	15nF (153)						Т —	Т —						
	22nF (223)	T	Т	Т			Т	T						
	33nF (333)						-	-						
	47nF (473)						-	-						
0	56nF (563)						 -	 						
apa	68nF (683)							 	- <b>-</b>	<b>–</b>				
lcita	100nF (104)							1 - T	і т*	- 1				
Inco	220nF (224)						і т*	і т*	I					
æ	330nF (334)						і Т*	і Т*						
	4/UNF (4/4)						т*	т*	T*					
	1.0UF (105)						T*	т*						
	2.2UF (225)												т	т
	0.3 ~ 10pr												т Т	T
	12pF												т Т	T
	19pF												T	T
	22nF												T	Т
	33nF												Т	Т
	39nF												Т	Т
	47pF												Т	Т
	56pF											Т	Т	Т
	68pF											Т	Т	Т
	82pF											Т	Т	Т
	100pF											Т	Т	Т
	120pF											Т	Т	Т
	150pF											Т	Т	Т
	270pF											Т	Т	
	330pF											Т	Т	
	390pF											Т	Т	
	470pF											Т	Т	
	560pF											Т	Т	

## **Reliability Test Condition and Requirements:**

No.	ltem	Test Cor	nditions	Requirements			
1.	Visual and			* No remarkable defect.			
	Mechanical			* Dimensions to c	onform to individ	ual specification s	heet.
2.	Capacitance	Class I: NP0		* Shall not exceed the limits given in the detailed spec.			ec.
3.	Q/ D.F.	1.0 ± 0.2Vrms, 1MHz ± 10%		* NP0: Cap $\ge$ 30pF. Q $\ge$ 1000: Cap<30pF. Q $\ge$ 400+20C			100+20C
	(Dissipation	Class II: X7R, X5R:		X7R, X5R:			
	Factor)	1.0 ± 0.2Vrms, 1kHz ± 10%		Rated Voltage	D.F.	Rated Voltage	D.F.
				≥50V	≦ 3%	10V	≦ 5.0%
				25V	≦ 3.5%	6.3V	≦ 10%
				16V	≦ 3.5%		
4.	Dielectric	* To apply voltage: ≤50V, 250%	of rated voltage.	* No evidence of	damage or flash (	over during test.	
	Strength	* Duration: 1 to 5 sec.	<sup>o</sup>				
		* Charge and discharge current	less than 50mA.				
L							
5.	Insulation	To apply rated voltage for max.	120 sec.	≥10GΩ or RxC≥5	00Ω-F whichever	is smaller	
	Resistance			Class II : X7R, X5	R, 6.3V ≥100Ω-F		
6.	Temperature	With no electrical load.		* Capacitance cha	ange:		
	Coefficient			NP0(C0G) : withir	±30ppm/°C		
		T.C.	Operating Temp.	X7R : within	n ± 15%		
		NP0(C0G)	-55 ~ 125℃ at 25℃	X5R : within	n ± 15%		
		X7R	-55 ~ 125℃ at 25℃				
		X5R	-55 ~ 85℃ at 25℃				
7.	Adhesive	* Pressurizing force :		* No remarkable of	damage or remov	al of the terminati	ons.
	Strength of	5N≤0603: 10N>0603					
	Termination	* Test time: 10±1 sec.					
8.	Vibration	* Vibration frequency: 10~55 Hz	/min.	* No remarkable damage.			
	Resistance	* Total amplitude: 1.5mm		* Cap change and Q/D.F.: To meet initial spec.			
		* lest time: 6 hrs. (Iwo hrs each	in three mutually				
0		perpendicular directions.)		050/	<b>.</b>		
9.	Solderability	* Display time: 210 5 and		95% min. coveraç	ge of all metalized	d area.	
10	Ponding Tost	* The middle part of substrate of		* No romarkable	damaga		
10.	bending rest	of the pressurizing rod at a rate	of about 1 mm per second until	* Can change:	Jamage.		
		the deflection becomes 1 mm a	nd then the pressure shall be	NPO: within +5.0	% or +0.5pF whic	hever is larger	
		maintained for 5±1 sec.		X7R: within ±12.5	i%	lever le larger.	
		* Measurement to be made afte	r keeping at room temp, for	Y5V: within ±30%			
		24±2 hrs.	5 F 5 F 5 F F	(This capacitance	change means t	he change of cap	acitance under
				specified flexure	of substrate from	the capacitance r	neasured before
				test.)			
11.	Resistance to	* Solder temperature: 270±5°C		* No remarkable	damage.		
	Soldering Heat	* Dipping time: 10±1 sec		* Cap change:			
		* Preheating: 120 to 150°C for 1	minute before immerse the	NPO: within ±2.5	% or ±0.25pF whi	ichever is larger.	
		capacitor in a eutectic solder.		X7R: within ±7.5%	6		
		*Before initial measurement (Cla	ass II) only): Perform 150	X5R: within ±7.5%	6		
		+0/-10°C for 1hr and then set fo	r 48±4hrs at room temp.	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.			equirements.
		* Measurement to be made afte	r keeping at room temp. for	* 25% max. leach	ing on each edge	9.	
		24±2 hrs.(Class I) or 48±4hrs.(C	Class II)				
12.	Temperature	* Conduct the five cycles accord	ling to the temperatures and	* No remarkable o	lamage.		
	Cycle	time.		Cap change :	· · · ·		
		*Before initial measurement (Cla	ass II) only): Perform 150	NPO: within ±2.5	% or ±0.25pF whi	chever is larger.	
		+U/-10°C for 1hr and then set fo	r 48±4hrs at room temp.	$X/R$ : within $\pm 7.5\%$	/o /		
		Nieasurement to be made afte	r keeping at room temp. for	$x_{0}$ × $x_{0}$ × $x_{0}$ × $x_{0}$ × $x_{0}$ × $x_{0}$ × $x_{0}$	<sup>(</sup> )	To most initial	
		$24\pm2$ nrs.(Class I) or 48±4hrs.(C	lass II)	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.			

### **Reliability Test Condition and Requirements:**

No.	Item	Test Condition	Requirements
13.	Humidity	Test temp.: 40±2°C	* No remarkable damage.
	(Damp Heat)	Humidity: 90~95% RH	* Cap change:
	Steady State	Test time: 500+24/-0hrs.	NPO: within ±5.0% or ±0.5pF whichever is larger.
		Measurement to be made after keeping at room temp. for	X7R,X5R: $\geq$ 10V, within ±12.5%, 6.3V, within ±25%
		24±2 hrs.(Class I) or 48±4hrs.(Class II)	* Q/D.F. value:
			NPO: Cap≥30pF, Q≥350; 10pF≤Cap<30pF, Q≥275+2.5C
			Cap<10pF; Q≥200+10C
			X7R,X5R : Ur=50V, ≦ 6.0% Ur=16, 25V, ≦ 5.0%
			Ur=10V, ≦ 7.5% Ur=6.3V, ≦ 15%
			<sup>★</sup> I.R.: ≥10V. ≥1GΩ or RxC≥50Ω-F whichever is smaller
			6.3V≥10Ω-F
14.	Humidity	Test temp.: 40±2°C	No remarkable damage.
	(Damp Heat)	Humidity: 90~95%RH	* Cap change:
	Load	Test time: 500+24/-0 hrs.	NPO: within ±5.0% or ±0.5pF whichever is larger.
		To apply voltage : rated voltage	X7R,X5R: ≧ 10V, within ±12.5%, 6.3V, within ±25%
		Measurement to be made after keeping at room temp. for	* Q/D.F. value:
		24±2 hrs.(Class I) or 48±4hrs.(Class II)	NPO: Cap≥30pF, Q≥350; 10pF≤Cap<30pF, Q≥275+2.5C
			Cap<10pF; Q≥200+10C
			X7R,X5R : Ur=50V, ≦ 6.0% Ur=16, 25V, ≦ 5.0%
			Ur=10V, ≦ 7.5% Ur=6.3V, ≦ 15%
			* I.R.: $\geq$ 10V. $\geq$ 1G $\Omega$ or RxC $\geq$ 25 $\Omega$ -F whichever is smaller
			6.3V ≥5Ω-F
15.	High	* Test temp.: NPO, X7R : 125±3°C, X5R: 85±3°C	* No remarkable damage.
	Temperature	To apply voltage:	* Cap change:
	Load	(1) $6.3V : 150\%$ of rated voltage.	NPO: within ±5.0% or ±0.5pF whichever is larger.
	(Endurance)	(2) >6.3V: 200% of rated voltage	$X/R, X5R: \leq 100$ , within $\pm 12.5\%$ , 6.3V, within $\pm 25\%$
		lest time: 1000+24/-0 hrs.	
		Measurement to be made after keeping at room temp. for	NPO: Cap≥30pF, Q≥350; 10pF≤Cap<30pF, Q≥275+2.5C
		24±2 nrs.(Class I) or 48±4nrs.(Class II)	
			$X/R, X5K : UI = 50V, \ge 6.0\% UI = 16, 25V, \ge 5.0\%$
			UI = 10V, = 7.5% $UI = 0.3V, = 15%$
			T.K.: 210V. 21GD of RXC225D-F whichever is smaller
			0.3V ≥5Ω-F

## **APPENDIXES**

### Tape & reel dimensions



Size	0201
Thickness	0.30±003
Ao	0.38±0.05
B₀	0.68±0.05
Т	0.42±0.05
K₀	-
W	8.00±0.10
P₀	4.00±0.10
10xP₀	40.0±0.10
P1	2.00±0.05
P <sub>2</sub>	2.00±0.05
D <sub>0</sub>	1.55±0.05
<b>D</b> 1	-
E	1.75±0.05
F	3.50±0.05

Size	02	0201					
Reel size	7"	13"					
С	13.0+0.5/-0.2	13.0+0.5/-0.2					
<b>W</b> 1	8.4+1.5/-0	8.4+1.5/-0					
Α	178.0±1.0	330.0±1.0					
N	60.0+1.0/-0	100±1.0					

#### Storage and handling conditions

- (1) To store products at 5 to 40 C ambient temperature and 20 to 70%. related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid. It may cause oxidization of electrode, which easily be resulted in poor soldering.
- b. To store products on the shelf and avoid exposure to moisture.
- c. Don't expose products to excessive shock, vibration, direct sunlight and so on.

#### **Recommended soldering conditions**

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of  $N_2$  within oven are recommended.

