

HITANO ENTERPRISE CORP.

FR151-FR157

TECHNICAL SPECIFICATIONS OF FAST RECTIFIER VOLTAGE RANGE – 50 to 1000 Volts CURRENT – 1.5 Amperes

FEATURES

*Fast switching

*Low leakage

*Low forward voltage drop

*High current capability

*High current surge

*High reliability

MECHANICAL DATA

*Case: Molded plastic

*Epoxy: UL 94V-0 rate flame retardant

*Lead: MIL-STD-202E, Method 208 guaranteed

*Mounting position : Any

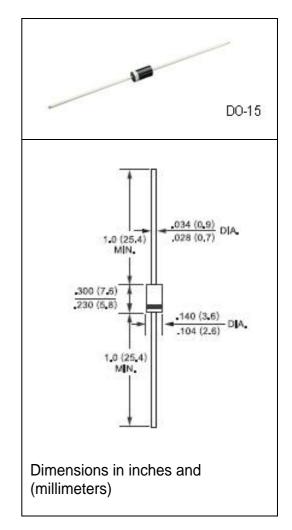
*Weight: 0.38 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 $^{\circ}$ C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.



	SYMBOL	FR151	FR152	FR153	FR154	FR155	FR156	FR157	UNIT S
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	Valts
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	Valts
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	Valts
Maximum Average Forward Rectified Current At $T_A = 75^{\circ}C$	Io	1.5							Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	60							Amps
Maximum instantaneous Forward Voltage at 1.5A DC	V_{F}	1.3							Valts
Maximum DC Reverse Current at Rated DC Blocking Voltage T _A =25°C	5.0								uAmp s
Maximum Full Load Reverse Current Full Cycle Average, .375*(9.5mm) lead length at TL=55°C	I _R	100							uAmp s
Maximum Reverse Recovery Time (Note 1)	trr	1:	50		250		500		nSec
Typical Junction Capacitance (Note 2)	C₃	40							pF
Operating and Storage Temperature Range	T_{J}, T_{STG}	-65 to +150							$^{\circ}\mathbb{C}$

NOTES: 1. Test Conditions : $I_F = 0.5A$, $I_R = 1.0A$, $I_{RR} = 0.25A$

2. Measured at 1MHz and applied reverse voltage of 4.0 volts.

RATING AND CHARACTERISTIC CURVES (FR151 THRU FR157)

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

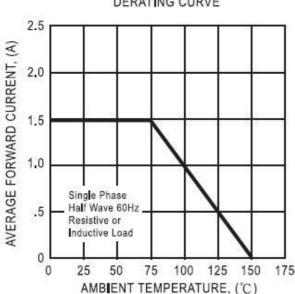


FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

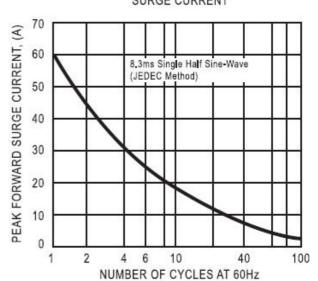


FIG. 3 - TYPICAL INSTANTANEOUS

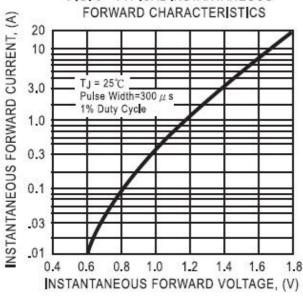


FIG. 4 - TYPICAL JUNCTION CAPACITANCE

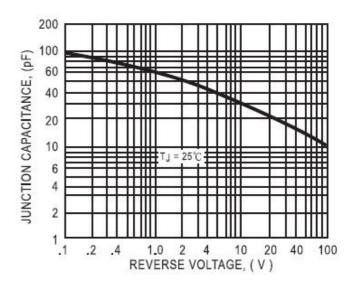


FIG. 5 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

